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APPLICANTS

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** CONTINUING DATA ***** *VFI LV*
 This application is a CON of 09/965,220 09/28/2001 PAT 6,723,631

** FOREIGN APPLICATIONS ***** *NONE LV*
 JAPAN 2000-300853 09/29/2000

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>[Signature]</i> Examiner's Signature	<input type="checkbox"/> no <input checked="" type="checkbox"/> no <i>LV</i> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 79	TOTAL CLAIMS 16	INDEPENDENT CLAIMS 1
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TITLE
 Fabrication method of semiconductor integrated circuit device

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)